

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	10/762380	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 09:18
L2	1	10/612676	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 09:56
L3	759	(buried near3 (hole pad plug stud pillar column\$3 contact)) with (bit adj line bitline)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 10:09
L4	232	3 and (interconnect\$3 "SAC" self near2 aligned near3 contact\$3)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 10:07
L5	230	4 and (dielectric interlayer "ILD" insulat\$3)	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 10:08
L6	77	5 and (buried near3 (hole pad plug stud pillar column\$3 contact)) with capacitor	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 10:10
L7	45	6 and (buried near3 (hole pad plug stud pillar column\$3 contact)) with etch\$3	US-PGPU B; USPAT; EPO; JPO	OR	ON	2005/08/04 10:10